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[0026]

Figs. 9 to 11 show circuit configurations each corresponding to one pixel of a light emitting panel according to another embodiment of the present invention. Each embodiment is different from the foregoing embodiment in a point that a switch circuit 32 has a FET 37 connected to an organic EL element 15 in series. That is, when the FET 37 becomes nonconductive in response to a control signal supplied to a gate G of the FET 37, light emission by the organic EL element 15 is stopped.

[0027]

According to the present invention, the aforementioned operation allows control of the light emission during an optional light emission period which is shorter than the address period in each sub field. Thus, it is possible to realize wider gradation display. In the foregoing embodiments, the respective numeric values are merely examples and, therefore, may be changed appropriately. In addition, the various switching circuits and the like may be appropriately used in combination.

Figs. 9 to 11

(1) Address line

- (2) Data line
- (3) EL element
- (4) Common electrode
- (5) Control signal
- (6) One pixel